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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:	Jyunichi NAKAMURA et al.	Confirmation No.:	5720
Serial No.:	10/649,745	Art Unit:	2826
Filed:	August 28, 2003	Examiner:	A. WILLIAMS
For:	MULTILAYERED SUBSTRATE FOR SEMICONDUCTOR DEVICE	Attorney Docket No:	060914-0117-US

RESPONSE TO RESTRICTION REQUIREMENT

U.S. Patent and Trademark Office
2011 South Clark Place, Customer Window
MAIL STOP AMENDMENT
Crystal Plaza Two, Room 1B03
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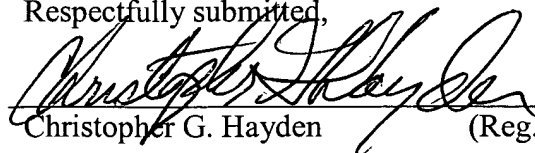
Sir:

In response to the outstanding Restriction Requirement dated August 25, 2004, Applicants hereby elect, without traverse, inter-related Figures 1, 2 and 8, readable on claims 1, 12, 13, 14 and 15. The Examiner is kindly requested to contact the undersigned at the number listed below should there be any clarification necessary.

No fees are believed due for this response. Should any fees be due, however, please charge the required amount to Morgan, Lewis & Bockius LLP Deposit Account No. 50-0310.

Date: September 22, 2004

Respectfully submitted,


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